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| Cut Sets for Gate 2\_HDG\_ERR  Date: 15/04/2025  <ProjectID>  RWB V15.0  C:\Users\50161\Desktop\TRY\_IMBSA\_v2\_ONLY\_ERR.rwb  Page 1 of 4 | | | |
| **Number** | **Cut set** | **Unavailability** | **Frequency** |
| 1 | AV.MCP.ERR | 0e+0 | 0e+0 |
| 2 | AV.FCC1.FPGA1.ERR.AV.FCC1.FPGA2.ERR | 0e+0 | 0e+0 |
| 3 | AV.FCC1.CPU1.ERR.AV.FCC1.FPGA2.ERR | 0e+0 | 0e+0 |
| 4 | AV.FCC1.FPGA1.ERR.AV.FCC1.CPU2.ERR | 0e+0 | 0e+0 |
| 5 | AV.FCC1.CPU1.ERR.AV.FCC1.CPU2.ERR | 0e+0 | 0e+0 |
| 6 | AV.FCC1.FPGA1.ERR.AV.FCC1.PS.LOSS | 0e+0 | 0e+0 |
| 7 | AV.FCC1.CPU1.ERR.AV.FCC1.PS.LOSS | 0e+0 | 0e+0 |
| 8 | AV.FCC1.FPGA1.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 9 | AV.FCC1.FPGA1.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 10 | AV.FCC1.FPGA1.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 11 | AV.FCC1.FPGA1.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 12 | AV.FCC1.FPGA1.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 13 | AV.FCC1.FPGA1.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 14 | AV.FCC1.FPGA2.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 15 | AV.FCC1.FPGA2.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 16 | AV.FCC1.FPGA2.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 17 | AV.FCC1.FPGA2.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 18 | AV.FCC1.FPGA2.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 19 | AV.FCC1.FPGA2.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 20 | AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.ERR.AV.FCC1.FPGA1.LOSS | 0e+0 | 0e+0 |
| 21 | AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.ERR.AV.FCC1.FPGA2.LOSS | 0e+0 | 0e+0 |
| 22 | AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.ERR.AV.FCC1.FPGA1.LOSS | 0e+0 | 0e+0 |
| 23 | AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.ERR.AV.FCC1.FPGA2.LOSS | 0e+0 | 0e+0 |
| 24 | AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ERR.AV.FCC1.FPGA1.LOSS | 0e+0 | 0e+0 |
| 25 | AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ERR.AV.FCC1.FPGA2.LOSS | 0e+0 | 0e+0 |
| 26 | AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.ERR.AV.FCC1.FPGA1.LOSS | 0e+0 | 0e+0 |
| 27 | AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.ERR.AV.FCC1.FPGA2.LOSS | 0e+0 | 0e+0 |
| 28 | AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS.AV.FCC1.FPGA1.LOSS | 0e+0 | 0e+0 |
| 29 | AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS.AV.FCC1.FPGA2.LOSS | 0e+0 | 0e+0 |
| 30 | AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOSS.AV.FCC1.FPGA1.LOSS | 0e+0 | 0e+0 |
| 31 | AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOSS.AV.FCC1.FPGA2.LOSS | 0e+0 | 0e+0 |
| 32 | AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.ERR.AV.FCC1.CPU1.LOSS | 0e+0 | 0e+0 |
| 33 | AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.ERR.AV.FCC1.CPU2.LOSS | 0e+0 | 0e+0 |
| 34 | AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.ERR.AV.FCC1.CPU1.LOSS | 0e+0 | 0e+0 |
| 35 | AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.ERR.AV.FCC1.CPU2.LOSS | 0e+0 | 0e+0 |
| 36 | AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ERR.AV.FCC1.CPU1.LOSS | 0e+0 | 0e+0 |
| 37 | AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ERR.AV.FCC1.CPU2.LOSS | 0e+0 | 0e+0 |
| 38 | AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.ERR.AV.FCC1.CPU1.LOSS | 0e+0 | 0e+0 |
| 39 | AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.ERR.AV.FCC1.CPU2.LOSS | 0e+0 | 0e+0 |
| 40 | AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS.AV.FCC1.CPU1.LOSS | 0e+0 | 0e+0 |
| 41 | AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS.AV.FCC1.CPU2.LOSS | 0e+0 | 0e+0 |
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| Cut Sets for Gate 2\_HDG\_ERR  Date: 15/04/2025  <ProjectID>  RWB V15.0  C:\Users\50161\Desktop\TRY\_IMBSA\_v2\_ONLY\_ERR.rwb  Page 2 of 4 | | | |
| **Number** | **Cut set** | **Unavailability** | **Frequency** |
| 42 | AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOSS.AV.FCC1.CPU1.LOSS | 0e+0 | 0e+0 |
| 43 | AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOSS.AV.FCC1.CPU2.LOSS | 0e+0 | 0e+0 |
| 44 | AV.FCC1.CPU1.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 45 | AV.FCC1.CPU1.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 46 | AV.FCC1.CPU1.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 47 | AV.FCC1.CPU1.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 48 | AV.FCC1.CPU1.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 49 | AV.FCC1.CPU1.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 50 | AV.FCC1.CPU2.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 51 | AV.FCC1.CPU2.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 52 | AV.FCC1.CPU2.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 53 | AV.FCC1.CPU2.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 54 | AV.FCC1.CPU2.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 55 | AV.FCC1.CPU2.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 56 | AV.AHRS1.ERR.AV.AHRS2.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.E  RR | 0e+0 | 0e+0 |
| 57 | AV.AHRS1.ERR.AV.AHRS2.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.  ERR | 0e+0 | 0e+0 |
| 58 | AV.AHRS1.ERR.AV.AHRS2.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ER  R | 0e+0 | 0e+0 |
| 59 | AV.AHRS1.ERR.AV.AHRS2.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.E  RR | 0e+0 | 0e+0 |
| 60 | AV.AHRS1.ERR.AV.AHRS2.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 61 | AV.AHRS1.ERR.AV.AHRS2.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOS  S | 0e+0 | 0e+0 |
| 62 | AV.AHRS2.ERR.AV.AHRS1.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.E  RR | 0e+0 | 0e+0 |
| 63 | AV.AHRS2.ERR.AV.AHRS1.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.  ERR | 0e+0 | 0e+0 |
| 64 | AV.AHRS2.ERR.AV.AHRS1.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ER  R | 0e+0 | 0e+0 |
| 65 | AV.AHRS2.ERR.AV.AHRS1.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.E  RR | 0e+0 | 0e+0 |
| 66 | AV.AHRS2.ERR.AV.AHRS1.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 67 | AV.AHRS2.ERR.AV.AHRS1.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOS  S | 0e+0 | 0e+0 |
| 68 | AV.AHRS1.ERR.AV.AHRS3.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.E  RR | 0e+0 | 0e+0 |
| 69 | AV.AHRS1.ERR.AV.AHRS3.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.  ERR | 0e+0 | 0e+0 |
| 70 | AV.AHRS1.ERR.AV.AHRS3.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ER  R | 0e+0 | 0e+0 |
| 71 | AV.AHRS1.ERR.AV.AHRS3.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.E  RR | 0e+0 | 0e+0 |
| 72 | AV.AHRS1.ERR.AV.AHRS3.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 73 | AV.AHRS1.ERR.AV.AHRS3.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOS  S | 0e+0 | 0e+0 |
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| Cut Sets for Gate 2\_HDG\_ERR  Date: 15/04/2025  <ProjectID>  RWB V15.0  C:\Users\50161\Desktop\TRY\_IMBSA\_v2\_ONLY\_ERR.rwb  Page 3 of 4 | | | |
| **Number** | **Cut set** | **Unavailability** | **Frequency** |
| 74 | AV.AHRS3.ERR.AV.AHRS1.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.E  RR | 0e+0 | 0e+0 |
| 75 | AV.AHRS3.ERR.AV.AHRS1.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.  ERR | 0e+0 | 0e+0 |
| 76 | AV.AHRS3.ERR.AV.AHRS1.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ER  R | 0e+0 | 0e+0 |
| 77 | AV.AHRS3.ERR.AV.AHRS1.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.E  RR | 0e+0 | 0e+0 |
| 78 | AV.AHRS3.ERR.AV.AHRS1.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 79 | AV.AHRS3.ERR.AV.AHRS1.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOS  S | 0e+0 | 0e+0 |
| 80 | AV.AHRS2.ERR.AV.AHRS3.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.E  RR | 0e+0 | 0e+0 |
| 81 | AV.AHRS2.ERR.AV.AHRS3.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.  ERR | 0e+0 | 0e+0 |
| 82 | AV.AHRS2.ERR.AV.AHRS3.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ER  R | 0e+0 | 0e+0 |
| 83 | AV.AHRS2.ERR.AV.AHRS3.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.E  RR | 0e+0 | 0e+0 |
| 84 | AV.AHRS2.ERR.AV.AHRS3.LOSS.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 85 | AV.AHRS2.ERR.AV.AHRS3.LOSS.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOS  S | 0e+0 | 0e+0 |
| 86 | AV.AHRS2.LOSS.AV.AHRS3.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.E  RR | 0e+0 | 0e+0 |
| 87 | AV.AHRS2.LOSS.AV.AHRS3.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.  ERR | 0e+0 | 0e+0 |
| 88 | AV.AHRS2.LOSS.AV.AHRS3.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ER  R | 0e+0 | 0e+0 |
| 89 | AV.AHRS2.LOSS.AV.AHRS3.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.E  RR | 0e+0 | 0e+0 |
| 90 | AV.AHRS2.LOSS.AV.AHRS3.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 91 | AV.AHRS2.LOSS.AV.AHRS3.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOS  S | 0e+0 | 0e+0 |
| 92 | AV.AHRS1.LOSS.AV.AHRS3.LOSS.AV.AHRS2.LOSS.AV.FCC2.FPGA1.ER  R.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 93 | AV.AHRS1.LOSS.AV.AHRS3.LOSS.AV.AHRS2.LOSS.AV.FCC2.FPGA1.ER  R.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 94 | AV.AHRS1.LOSS.AV.AHRS3.LOSS.AV.AHRS2.LOSS.AV.FCC2.CPU1.ERR.  AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 95 | AV.AHRS1.LOSS.AV.AHRS3.LOSS.AV.AHRS2.LOSS.AV.FCC2.CPU1.ERR.  AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 96 | AV.AHRS1.LOSS.AV.AHRS3.LOSS.AV.AHRS2.LOSS.AV.FCC2.CPU1.ERR.  AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 97 | AV.AHRS1.LOSS.AV.AHRS3.LOSS.AV.AHRS2.LOSS.AV.FCC2.FPGA1.ER  R.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 98 | AV.FCC1.PS.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 99 | AV.FCC1.PS.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 100 | AV.FCC1.PS.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.CPU2.ERR | 0e+0 | 0e+0 |
| 101 | AV.FCC1.PS.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.FPGA2.ERR | 0e+0 | 0e+0 |
| 102 | AV.FCC1.PS.ERR.AV.FCC2.CPU1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
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| Cut Sets for Gate 2\_HDG\_ERR  Date: 15/04/2025  <ProjectID>  RWB V15.0  C:\Users\50161\Desktop\TRY\_IMBSA\_v2\_ONLY\_ERR.rwb  Page 4 of 4 | | | |
| **Number** | **Cut set** | **Unavailability** | **Frequency** |
| 103 | AV.FCC1.PS.ERR.AV.FCC2.FPGA1.ERR.AV.FCC2.PS.LOSS | 0e+0 | 0e+0 |
| 104 | AV.AHRS2.LOSS.AV.AHRS1.LOSS.AV.AHRS3.ERR | 0e+0 | 0e+0 |
| 105 | AV.AHRS1.LOSS.AV.AHRS2.ERR.AV.AHRS3.LOSS | 0e+0 | 0e+0 |
| 106 | AV.AHRS2.LOSS.AV.AHRS1.ERR.AV.AHRS3.LOSS | 0e+0 | 0e+0 |
| 107 | AV.AHRS1.ERR.AV.AHRS2.ERR | 0e+0 | 0e+0 |
| 108 | AV.AHRS3.ERR.AV.AHRS2.ERR | 0e+0 | 0e+0 |
| 109 | AV.AHRS3.ERR.AV.AHRS1.ERR | 0e+0 | 0e+0 |
| 110 | AV.GPS1.ERR.AV.GPS2.ERR | 0e+0 | 0e+0 |
| 111 | AV.TRA.SHAFT.ERR | 0e+0 | 0e+0 |
| 112 | AV.TRA.ELETTRONIC.ERR | 0e+0 | 0e+0 |
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